

	Hits	Search Text	DBs	Time Stamp
1	1707	prescaler	USPAT	2004/11/12 09:51
2	0	prescaler with non\$integer	USPAT	2004/10/29 10:17
3	241	prescaler with division	USPAT	2004/10/29 10:18
4	13	prescaler with division with integer	USPAT	2004/10/29 10:18
5	253	prescaler.clm.	USPAT	2004/10/29 10:32
6	7	prescaler.clm. with multiplex\$2	USPAT	2004/10/29 10:32
7	7	prescaler near2 architecture	USPAT	2004/10/29 12:39
8	22	prescaler with circuit with multiplex\$2	USPAT	2004/10/29 12:46
9	2	"6424192".URPN.	USPAT	2004/10/29 15:03
10	26	("4755774" "5012142" "5175729" "5179303" "5180994" "5408201" "5418815" "5438591" "5448772" "5507025" "5555182" "5584062" "5614868" "5734970" "5761617" "5794119" "5861773" "5872810" "5880613" "5889437" "5894592" "5950119" "5963855" "6084905" "6097768" "6194947") .PN.	USPAT	2004/10/29 15:04
11	2	prescaler and ((control adj signals) with (first adj clock))	USPAT	2004/11/10 16:33
12	1	(generat\$3 near3 (control adj signals)) with (first adj clock) same (multiplex\$3 with (second adj clock))	USPAT	2004/11/10 16:35
13	69	(generat\$3 near3 (control adj signals)) with (first adj clock)	USPAT	2004/11/10 16:55
14	0	(second adj frequency) with (non\$integer adj fraction) with (first adj frequency)	USPAT	2004/11/10 16:59
15	6	(non\$integer adj fraction) near3 frequency	USPAT	2004/11/10 16:59
16	7	prescaler and (non\$integer adj division)	USPAT	2004/11/10 17:05

	Hits	Search Text	DBs	Time Stamp
17	0	prescaler with (first adj circuit) with multiplex\$2 with (second adj circuit)	USPAT	2004/11/10 17:26
18	0	(frequency adj scal\$3) with (non\$1integer adj (division fraction))	USPAT	2004/11/10 17:27
19	1	(frequency adj scal\$3) with (integer adj (division fraction))	USPAT	2004/11/10 17:27
20	338	prescaler near2 (architecture design circuit\$2)	USPAT	2004/11/12 09:52
21	26	prescaler near2 (architecture design circuit\$2) and (frequency near2 scal\$3)	USPAT	2004/11/12 13:13
22	0	prescaler near2 (architecture design circuit\$2) with non\$1integer	USPAT	2004/11/12 10:09
23	10	prescaler near2 (architecture design circuit\$2) and non\$1integer	USPAT	2004/11/12 13:14
24	3	prescaler near2 (architecture design circuit\$2) and non\$1integer	US-PGPUB; EPO; JPO; IBM_TDB	2004/11/12 13:12
25	13	prescaler near2 (architecture design circuit\$2) and (frequency near2 scal\$3)	US-PGPUB; EPO; JPO; IBM_TDB	2004/11/12 13:13
26	7	prescaler near2 (architecture design circuit\$2) with fractional	USPAT	2004/11/12 13:27
27	3	prescaler near2 (architecture design circuit\$2) with fractional	US-PGPUB; EPO; JPO; IBM_TDB	2004/11/12 13:27
28	3	("6583674").URPN.	USPAT	2004/11/12 13:41
29	7	("4573176" "4586005" "4891774" "5394116" "5576666" "5714896" "5970110").PN.	US-PGPUB; USPAT; USOCR	2004/11/12 13:55